Applicable System

NSD's MELSEC-Q Built-in unit can be used in the following system.

■Target model

- VS-Q62(P) Series
- VS-Q62B(P) Series
- VS-Q262 Series
- VS-Q262B Series
- VS-Q262BH Series
- VS-QA62 Series
- VS-QA262 Series
- VS-QA262B Series

Contact your NSD representative if any other types of system than the ones shown below are to be used.

Applicable module *1			No. of modules *2	Remarks
Programmable controller CPU	Basic model QCPU	Q00JCPU	Up to 16	
		Q00CPU Q01CPU	Up to 24	
	High Performance model QCPU	Q02CPU Q02HCPU Q06HCPU Q12HCPU Q25HCPU	Up to 64	Can be installed only in the "Q" mode.
	Process CPU	Q02PHCPU Q06PHCPU Q12PHCPU Q25PHCPU	Up to 64	The online module cannot be replaced.
	Redundant CPU	Q12PRHCPU Q25PRHCPU	Up to 53	The online module cannot be replaced. Cannot be installed on a basic base equipped with a redundant CPU. Can be installed on any of the following base units. - Redundant power extension base unit (Q68RB) - Remote I/O station on MELSECNET/H remote I/O network
	Universal model QCPU	Q00UJCPU	Up to 16	
		Q00UCPU Q01UCPU	Up to 24	
		Q02UCPU	Up to 36	
		Q03UDCPU Q04UDHCPU Q06UDHCPU Q10UDHCPU Q13UDHCPU Q20UDHCPU Q26UDHCPU Q03UDECPU Q04UDEHCPU Q06UDEHCPU Q10UDEHCPU Q13UDEHCPU Q20UDEHCPU Q26UDEHCPU Q50UDEHCPU Q100UDEHCPU	Up to 64	
	High-Speed Universal model QCPU	Q03UDVCPU Q04UDVCPU Q06UDVCPU Q13UDVCPU Q26UDVCPU	Up to 64	
	C Controller CPU	Q06CCPU-V	Up to 64	
		Q06CCPU-V-B	Up to 64	The upper and lower bytes of the word data on the buffer memory are reversed.
		Q12DCCPU-V	Up to 64	
		Q24DHCCPU-V Q24DHCCPU-LS	Up to 62	
	Win CPU (Mitsubishi Electric Corporation)	Q01WCPU-W1-J Q01WCPU-W1-CF-J Q01WCPU-W1-E Q01WCPU-W1-CF-E	Up to 63	
	Win CPU (CONTEC Co., Ltd.)	PPC-100-DC5511 PPC-100-DC5311	Up to 63	
Remote I/O module		QJ72LP25-25 QJ72LP25G QJ72LP25GE QJ72BR15	Up to 64	MELSECNET/H remote I/O station

*1: Refer to your module's User's and/or Reference Manuals.

*2: Limited within the number range of I/O points for the CPU module or remote I/O module.

*3: NSD's MELSEC-Q Built-in unit cannot be used with a safety CPU and motion CPU.